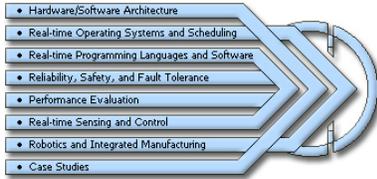




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- Hardware/Software Architecture
- Real-time Operating Systems and Scheduling
- Real-time Programming Languages and Software
- Reliability, Safety, and Fault Tolerance
- Performance Evaluation
- Real-time Sensing and Control
- Robotics and Integrated Manufacturing
- Case Studies

CALL FOR PAPERS
The 38th IEEE Real-Time Systems Symposium
December 5 - December 8, Paris, France
Sponsored by the IEEE Computer Society Technical Committee on Real-Time Systems



Scope of the Conference

The IEEE Real-Time Systems Symposium (RTSS) is the premier conference in the area of real-time systems, presenting innovations in the field with respect to theory and practice. RTSS provides a forum for the presentation of high-quality, original research covering all aspects of real-time systems theory, design, analysis, implementation, evaluation, and experiences. RTSS'17 continues the trend of making RTSS an expansive and inclusive symposium, looking to embrace new and emerging areas of real-time systems research.

RTSS'17 welcomes submissions in all areas of real-time systems, including but not limited to operating systems, networks, middleware, compilers, tools, modeling, scheduling, QoS support, resource management, testing and debugging, design and verification, hardware/software co-design, fault tolerance, security, power and thermal management, embedded platforms, and system experimentation and deployment experiences.

In addition to the main real-time track, we will continue the successful format of previous years with three specialized tracks:

- Cyber-physical systems
- HW-SW integration and system level design
- Internet of Things

Although authors will choose which tracks to submit papers to, a paper may be redirected to a more appropriate track. All papers will appear in the main program and proceedings. Papers in all tracks must address topics related to real-time systems.

Paper Submission Guidelines

All papers must be submitted electronically in PDF format, following the IEEE conference proceedings format and must describe original work not previously published or concurrently. The main body of each submitted paper is limited to 10 pages.

Important Dates

Submission Deadline (FIRM): May 1, 2017, 7:00 UTC
Rebuttal period: June 12 to June 15
Notification of Acceptance: July 10
Workshops : December 5
Symposium: December 6 to December 8

Organizing committee

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Frank Muller, North Carolina State University

Ex-Officio (TC Chair)

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Isabelle Puaut, Université de Rennes 1/IRISA

Finance Chair:

Karl-Erik Arzen, Lund University

Local arrangements chair:

Liliana Cucu-Grosjean, Inria Paris

Local information

The conference is organized in the neo-classical building of the « Saint Augustin » military cercle. Located in the heart of Paris, the well known chef of « Saint Augustin » reserves to his guests a exquisite visit of the French cuisine and its famous wines. The lounges inspired by the pure Versailles vision, complete the unique view of the French style that the conference participants will discover.



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